IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of Applicants:

Date: March 5, 2002

Beaman et al.

Dock t No.: YO993-028BX

Group Art Unit: 2829

Serial No.: 09/382,834

Filed: August 25, 1999

Examiner: V. P. Nguyen

HIGH DENSITY INTEGRATED CIRCUIT APPARATUS, TEST PROBE

AND METHODS OF USE THEREOF

**Assistant Commissioner for Patents** Washington, DC 20231

## CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this paper (1 page) is being facsimile transmitted under Rule 37 CFR 1.61(d) to the U.S. Pater and Trademark Office to (703) 308-7382 on March 5, 2002.

Dr. Daniel P. Morris, Est

FAX COPY RECEIVED

MAR 5 - 2002

Reg. No. 32,053

NOTICE OF APPEAL TO THE

TECHNOLOGY CENTER 2800

**BOARD OF APPEALS AND INTERFERENCES** 

Sir:

93/08/2008. TDAKKINS 000 Applicants hereby appeals to the Board of Appeals and Interferences from the Final Rejection of claims 29-60 in the Office Action dated October 5, 2001.

> Please charge any fee necessary to enter this paper and any previous paper to deposit account 09-0468.

> > Respectfully sybmitte

Dr. Daniel P. Morris, Esq. Registration No.: 32,053 Phone No.: 914-945-3217

**IBM Corporation** Intellectual Property Law Department P.O. Box 218 Yorktown Heights, New York 10598

TOTAL PAGE. 01 xxx

\$03/20/2002 SDFVIS 00000001 030468 09382834

01 FC:115

110.00 CH